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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/473,575	12/28/1999	Darrell D. Boggs	042390.P6871	042390.P6871 1163		
8791	7590 07/14/2003					
BLAKELY SOKOLOFF TAYLOR & ZAFMAN			EXAMI	EXAMINER		
	IIRE BOULEVARD, SI ES, CA 90025	EVENTH FLOOR	VO, LII	VO, LILIAN		
			ART UNIT	PAPER NUMBER		
			2127	9		
			DATE MAILED: 07/14/2003	, <i>J</i>		

Please find below and/or attached an Office communication concerning this application or proceeding.

				4-1-1
•	Арр	lication No.	Applicant(s)	
		473,575	BOGGS ET AL.	
Office Action Summary	Exa	miner	Art Unit	
		n Vo	2127	
The MAILING DATE of this comm Period for Reply	nunication appears (on the cover sheet w	vith the correspondence addre	ss
A SHORTENED STATUTORY PERIOR THE MAILING DATE OF THIS COMM - Extensions of time may be available under the provise after SIX (6) MONTHS from the mailing date of this control of the period for reply specified above is less than this lift NO period for reply is specified above, the maximuter of the period for reply within the set or extended period for Any reply received by the Office later than three mone earned patent term adjustment. See 37 CFR 1.704(the status)	UNICATION. sions of 37 CFR 1.136(a). In communication. ty (30) days, a reply within to m statutory period will apply treply will, by statute, cause this after the mailing date of	n no event, however, may a the statutory minimum of thi y and will expire SIX (6) MO the application to become A	reply be timely filed rty (30) days will be considered timely. NTHS from the mailing date of this comminible (35 U.S.C. § 133).	unication.
1) Responsive to communication(s	s) filed on <u>28 Decen</u>	<u>nber 1999</u> .		
2a) This action is FINAL .	2b)⊠ This acti	ion is non-final.		
3) Since this application is in condictored in accordance with the p Disposition of Claims				erits is
4) Claim(s) 1-83 is/are pending in t	he application.			
4a) Of the above claim(s) i	s/are withdrawn fro	m consideration.		
5) Claim(s) is/are allowed.				
6) Claim(s) is/are rejected.				
7) Claim(s) is/are objected to),			
8) Claim(s) 1-83 are subject to restr		on requirement.		
Application Papers		·		
9)☐ The specification is objected to by	the Examiner.			
10)☐ The drawing(s) filed on is/a	re: a) accepted or	b) objected to by	the Examiner.	
Applicant may not request that any	objection to the draw	ing(s) be held in abey	ance. See 37 CFR 1.85(a).	
11) The proposed drawing correction	filed on is: a)	☐ approved b)☐ o	disapproved by the Examiner.	
If approved, corrected drawings are	required in reply to the	nis Office action.		
12) ☐ The oath or declaration is objected	to by the Examine	r.		
Priority under 35 U.S.C. §§ 119 and 120				
13) Acknowledgment is made of a cla	aim for foreign priori	ty under 35 U.S.C.	§ 119(a)-(d) or (f).	
a) ☐ All b) ☐ Some * c) ☐ None o	f:			
1. Certified copies of the prior	ity documents have	been received.		
2. Certified copies of the prior	ity documents have	been received in A	pplication No	
3. Copies of the certified copiesapplication from the Interest See the attached detailed Office ac	ernational Bureau (I	PCT Rule 17.2(a)).	received in this National Stag	je
14) Acknowledgment is made of a clair	n for domestic prior	ity under 35 U.S.C.	§ 119(e) (to a provisional app	lication).
a) The translation of the foreign15) Acknowledgment is made of a clair		• •		
Attachment(s)	-			
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review 3) Information Disclosure Statement(s) (PTO-1449)			Summary (PTO-413) Paper No(s). 🔏 Informal Patent Application (PTO-15)	
S. Patent and Trademark Office TO-326 (Rev. 04-01)	Office Action Sur	mmary	Part of Paper No. 9	

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DETAILED ACTION

Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

Group I, claims 1 - 22 and 71 - 77, which drawn to a method and an apparatus for managing resources in a multithreaded processor comprising partitioning which sizing the corresponding portion for each thread according to a partitioning scheme, marking by specifying the lower and upper boundaries of each corresponding portion as being reserved, determining the type of the instructions to be executed for the allocated entries, comparing the value of the pointers to determine the availability for allocation, and wrapping the pointers when they advanced past the end of the respective portions and updating the wrap bits, classified in class 709, subclass 102.

Group II, claims 23 – 61 and 78 - 82, which drawn to a method and an apparatus for managing resources in a multithreaded processor comprising detecting a signal indicating a processing mode, which is updated in response to an occurrence of an event or a condition to perform resource allocation according to either single or multithread scheme, determining whether the set of instructions belongs to the respective thread by examining the value of the thread bit, and stalling with fetching of instructions from the respective thread if the stall signals is activated, classified in class 709, subclass 104.

Group III, claims 62 - 70, which drawn to a processor with an allocator, and an instruction delivery engine which fetches an invalid instruction to the allocator if the stall signal for the respective thread is activated, classified in class 712, subclass 1.

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2. The inventions are distinct, each from the other because of the following reasons:

Inventions I - III are related as subcombinations disclosed as usable together in a single

combination. The subcombinations are distinct from each other if they are shown to be

separately usable. In the instant case, invention I has separate utility such as in a system lacking

detecting a signal indicating a processing mode, which is updated in response to an occurrence of

an event or a condition to perform resource allocation according to either single or multithread

scheme, determining whether the set of instructions belongs to the respective thread by

examining the value of the thread bit, and stalling with fetching of instructions from the

respective thread if the stall signals is activated, and a processor with an allocator, and an

instruction delivery engine which fetches an invalid instruction to the allocator if the stall signal

for the respective thread is activated. Invention II has separate utility such as in a system lacking

a processor with an allocator, and an instruction delivery engine, which fetches an invalid

instruction to the allocator if the stall signal for the respective thread is activated. See MPEP §

806.05(d).

3. Because these inventions are distinct for the reasons given above and have acquired a

separate status in the art as shown by their different classification, restriction for examination

purposes as indicated is proper.

4. Because these inventions are distinct for the reasons given above and the search required

for one group is not required for another group, restriction for examination purposes as indicated

is proper.

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5. A telephone call was made to James Henry on 7/7/2003 to request an oral election to the

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above restriction requirement, but did not result in an election being made.

6. Applicants are advised that the reply to this requirement to be completed must include an

election of the invention to be examined even though the requirement be traversed (37 CFR

1.143).

7. Applicants are reminded that upon the cancellation of claims to a non-elected invention,

the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the

currently named inventors is no longer an inventor of at least one claim remaining in the

application. Any amendment of inventorship must be accompanied by a request under 37 CFR

1.48(b) and by the fee required under 37 CFR 1.17(i).

8. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Lilian Vo whose telephone number is 703-305-7864.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is 703-305-3900.

Lilian Vo Examiner

Examiner

Art Unit 2127

lv July 11, 2003

WILLIAM GRANT

SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2100

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